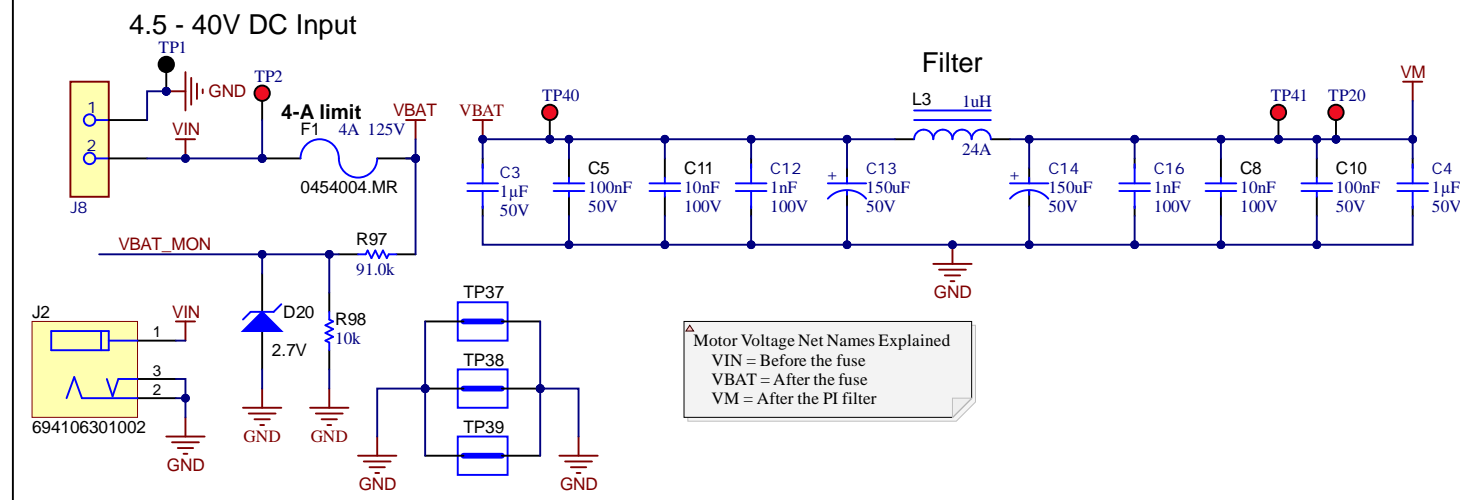
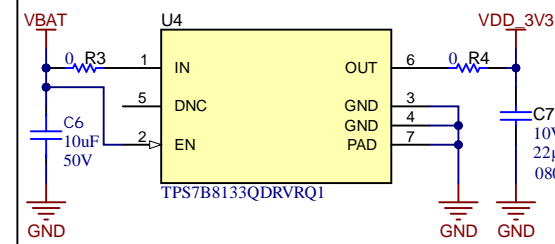


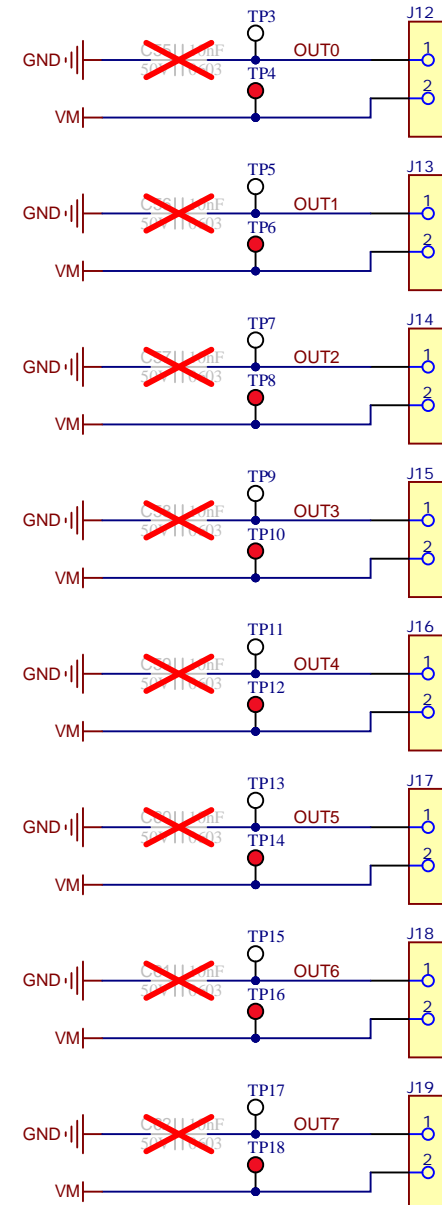
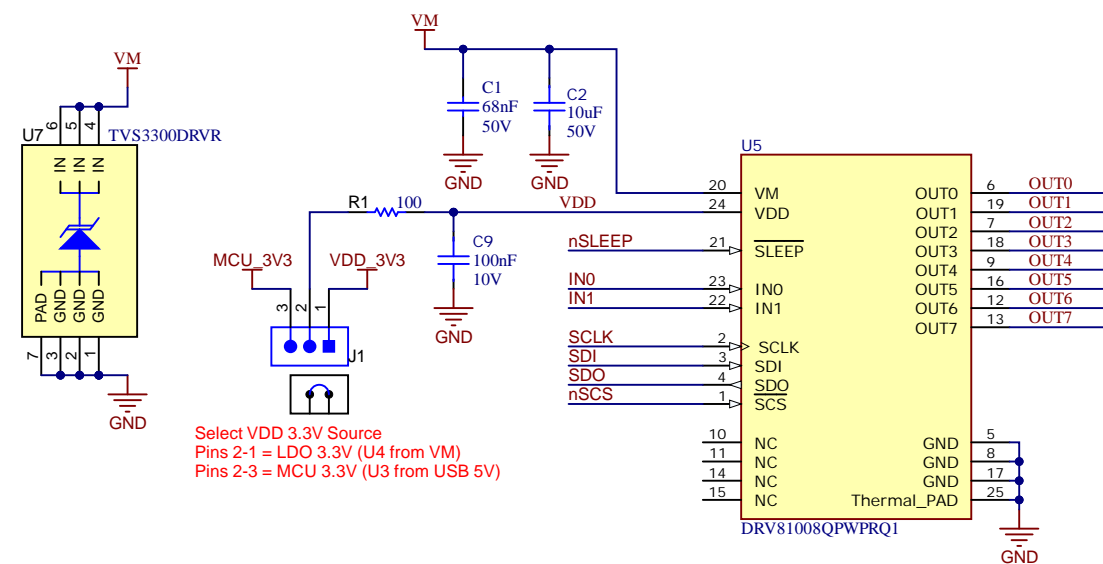
Board power



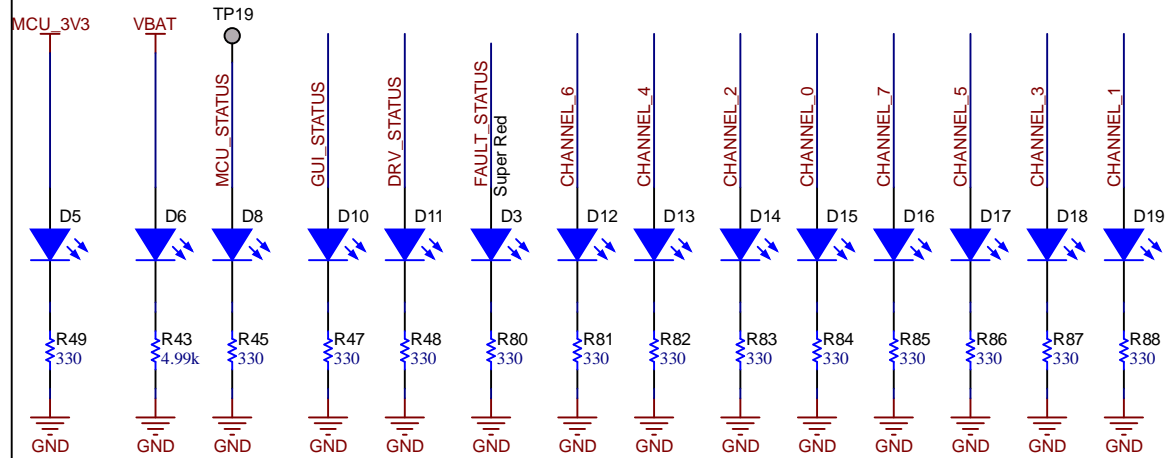
3.3V LDO



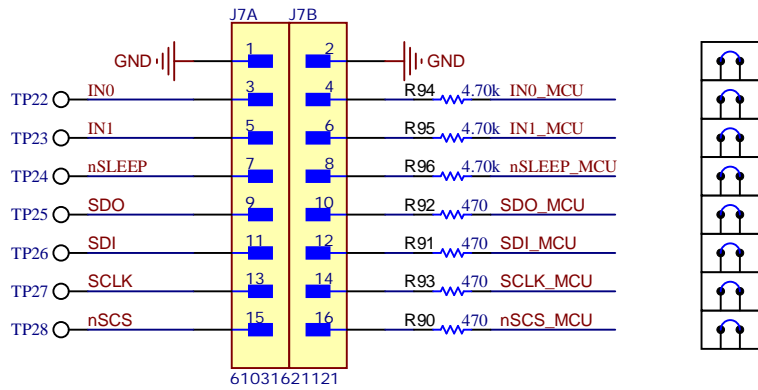
Output Connectors

**DRV81008-Q1**

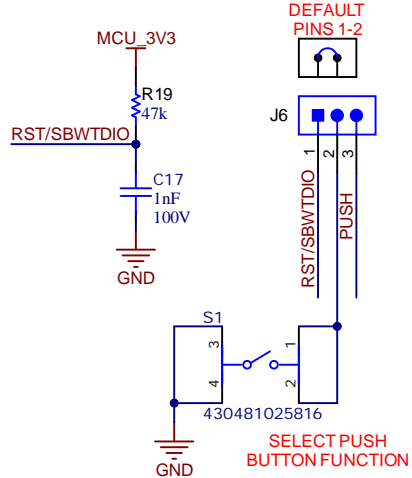
LEDS



Main Signal Header



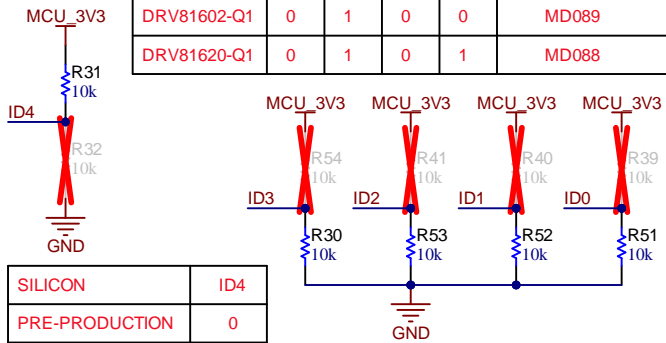
RST/PUSH Button



By default the pushbutton will send a reset/restart signal to the MCU.
If the jumper is in the "PUSH" position then the signal will go to a GPIO pin on the MCU to support custom firmware doing something else when the button is pressed.

ID Resistors

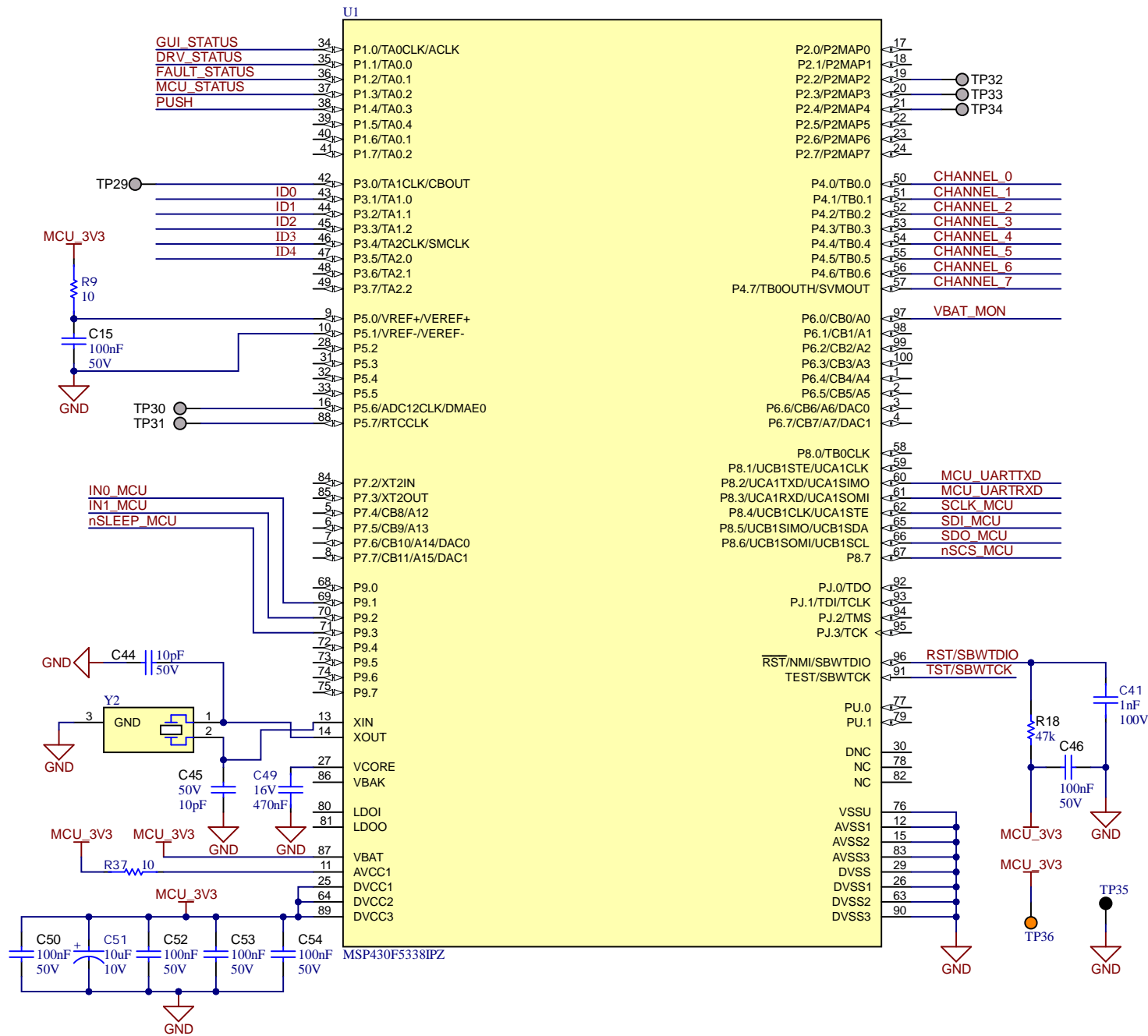
Device	ID3	ID2	ID1	ID0	MD #
DRV81008-Q1	0	0	0	0	MD075
DRV81004-Q1	0	0	0	1	MD086
DRV81080-Q1	0	0	1	0	MD081
DRV81242-Q1	0	0	1	1	MD091
DRV81602-Q1	0	1	0	0	MD089
DRV81620-Q1	0	1	0	1	MD088



SILICON	ID4
PRE-PRODUCTION	0
PRODUCTION	1

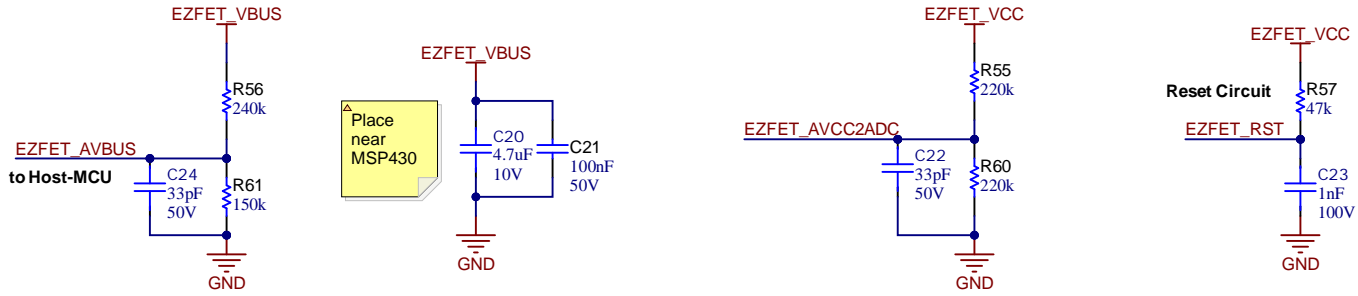
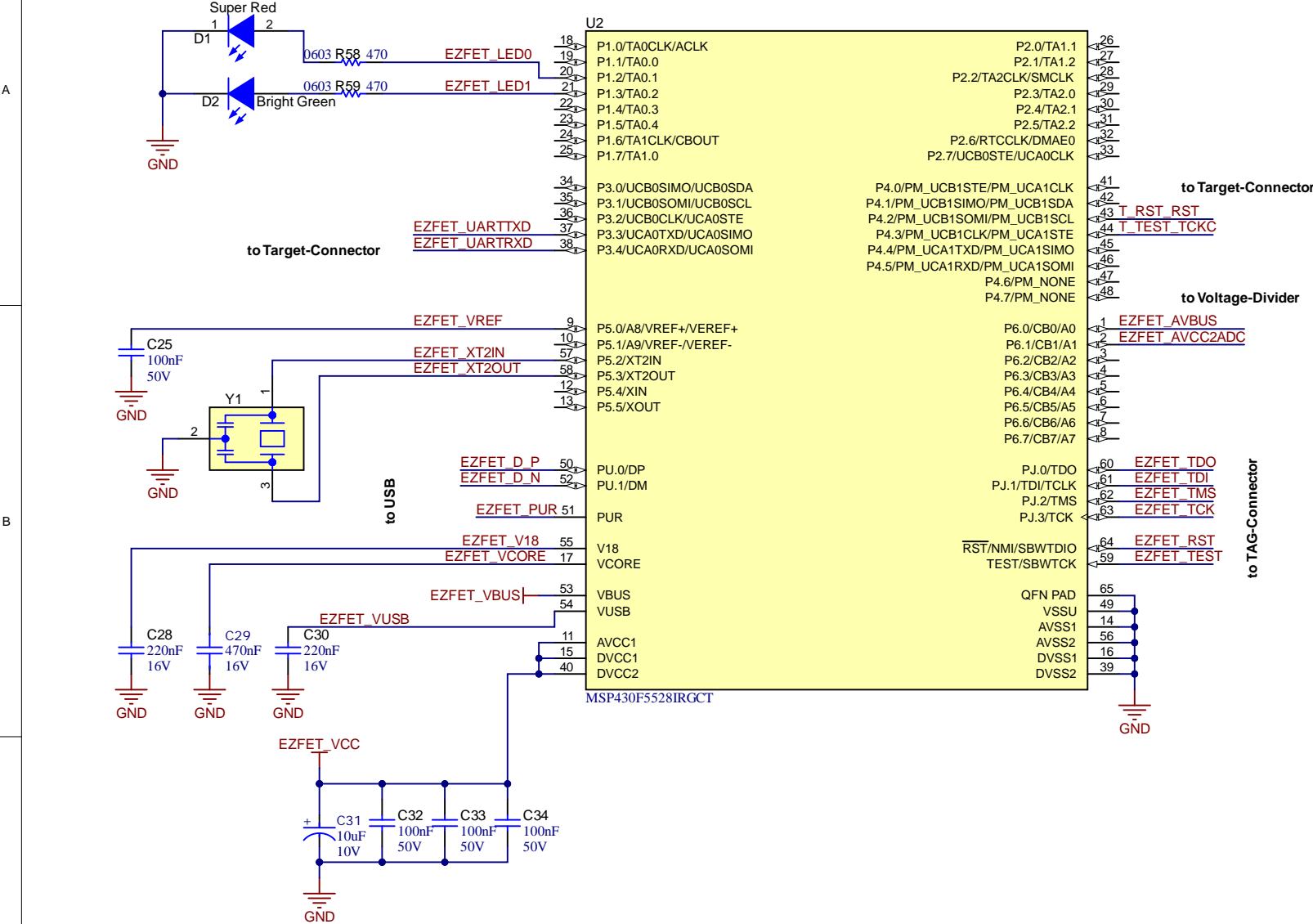
The resistors on the ID[3:0] nets inform the firmware which device ID variant is on this board

MSP430

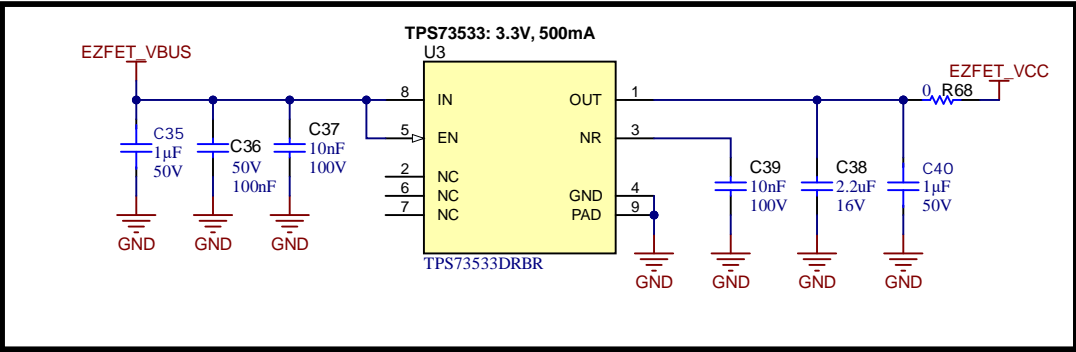


Orderable: DRV81008-Q1EVM	Designed for: Public Release	Mod. Date: 7/19/2024
TID #: N/A	Project Title: MD075	
Number: MD075	Rev: B	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 2 of 4
Drawn By: David Medis	File: MD075_MCU_SchDoc	Size: B
Engineer: David Medis	Contact: http://www.ti.com/support	

Host MCU for Emulation



3.3V Power (EZFET_VCC)

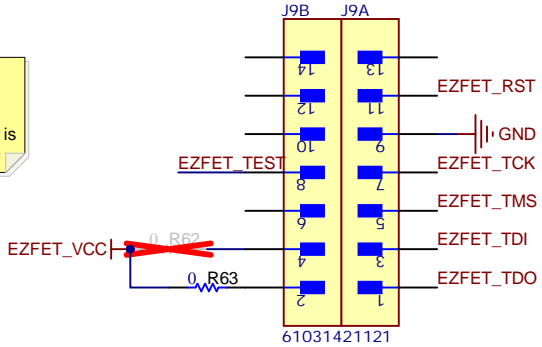


JTAG-Connector (Host Debug)

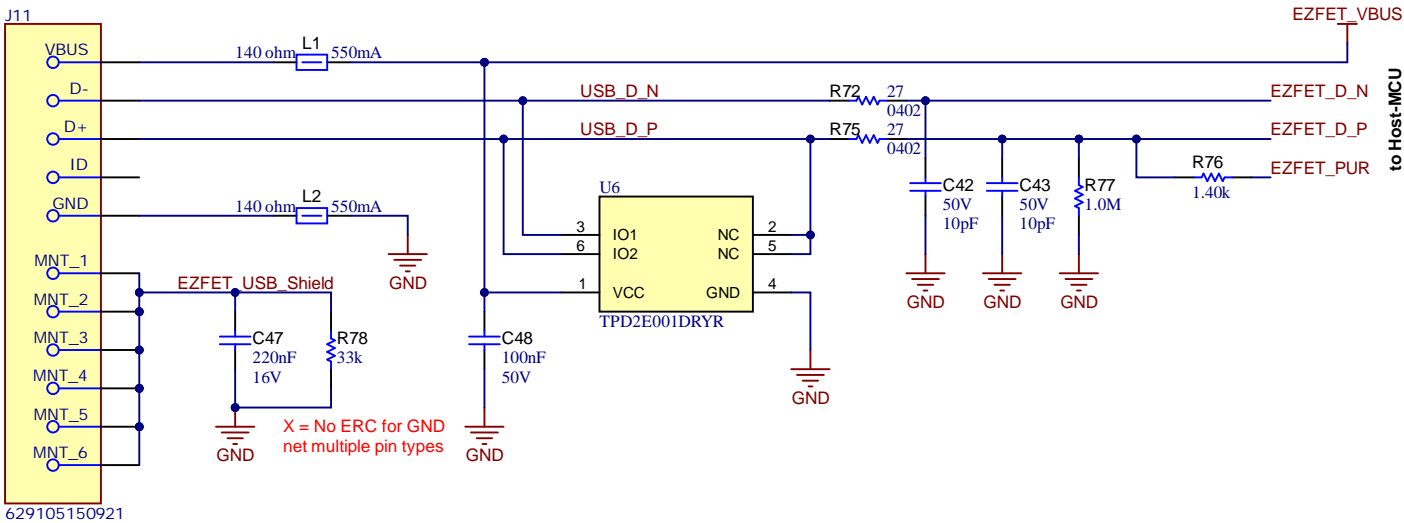
Host-MCU Debug Connector

POP Pin 2 zero ohm resistor if MSP-FET is supplying 3V3 to MSP430
POP Pin 4 zero ohm resistor if USB is supplying 3V3 to MSP430 (via LDO)

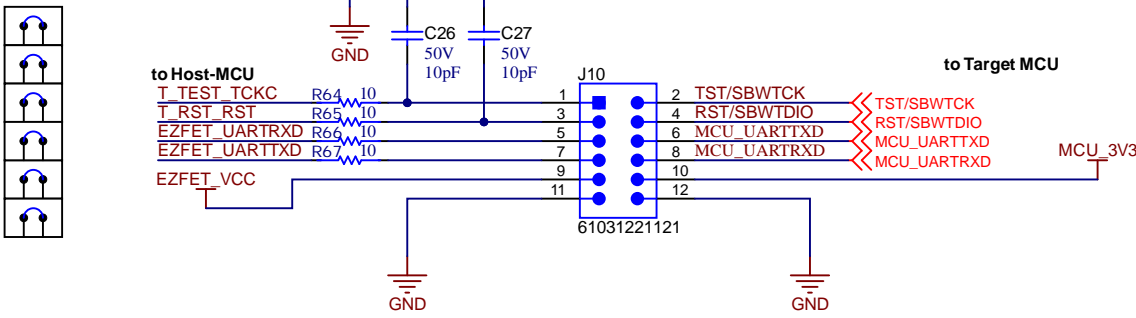
DNPed Pin 2 zero resistor. If debug or programming adapter is used to power the MSP430, remove Pin 4 resistor and populate Pin 2 resistor.



USB-I Interface



Target Connector



1

2

3

4

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